data being generated or modified by the first application process alone or also by other application processes running simultaneously with the first application process.

3. (Amended) The data base according to claim 2, wherein for a number of application processes running simultaneously, a control mechanism within the first application process, by exchanging messages with control mechanisms of the other application processes, controls accesses, required for loading the data to be persistently stored, of individual application processes running simultaneously, to the buffer using process identification numbers, entered in a shared memory, of the application processes running simultaneously.

4. (Amended) The data base according to claim 1, wherein all of the persistent data stored in the buffer is alternately written into one of the storage units or storage areas of the permanent memory.

8. (Amended) The data base according to claim 1, wherein only the persistent data, if necessary including reconstruction data, is transferred into the buffer from a first memory which contains a run-time program and associated permanent data.

9. (Amended) The data base according to claim 8, wherein the persistent data is stored in a space-saving manner as a data sequence in the buffer and in the permanent memory.

10. (Amended) The data base according to claim 1, wherein at least one further permanent memory is provided for a start program and application software including

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data base management software, with use of which configuration data to be written into the first memory is automatically reconstructed from the persistent [permanent] data stored in the permanent memory.

5   12. (Amended) The data base according to claim 1, wherein the buffer has at least two random access memories, functionally connected in series, persistent data stored in the first random access memory being written into the second random access memory so that the first random access memory is available for reloading while persistent [permanent] data from the second or a further random access memory is

10   written into the permanent memory.

16. (Amended) The data base according to claim 1, wherein a number of configuration changes are only performed at a data management side and thereafter at least one of a functional and a hardware change comprising all configuration changes is

15   performed in the terminal.

Please cancel claims 14 and 15 without prejudice.

REMARKS

Claims 1-16 are pending in the application. The claims / sections were rejected

20   as follows:

Claims / Section	35 U.S.C. Sec.	References / Notes
1	§102(b) Anticipation	o Pathakis, et al. (U.S. Patent No. 5,946,467).
2-16	§103(a) Obviousness	o Pathakis, et al. (U.S. Patent No. 5,946,467); and o Krueger, et al. (U.S. Patent No. 5,634,050).

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